Tablet Device

Solution Proposal by Toshiba



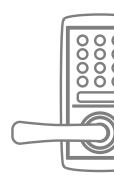






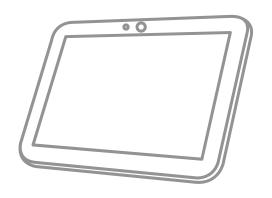




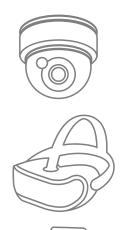








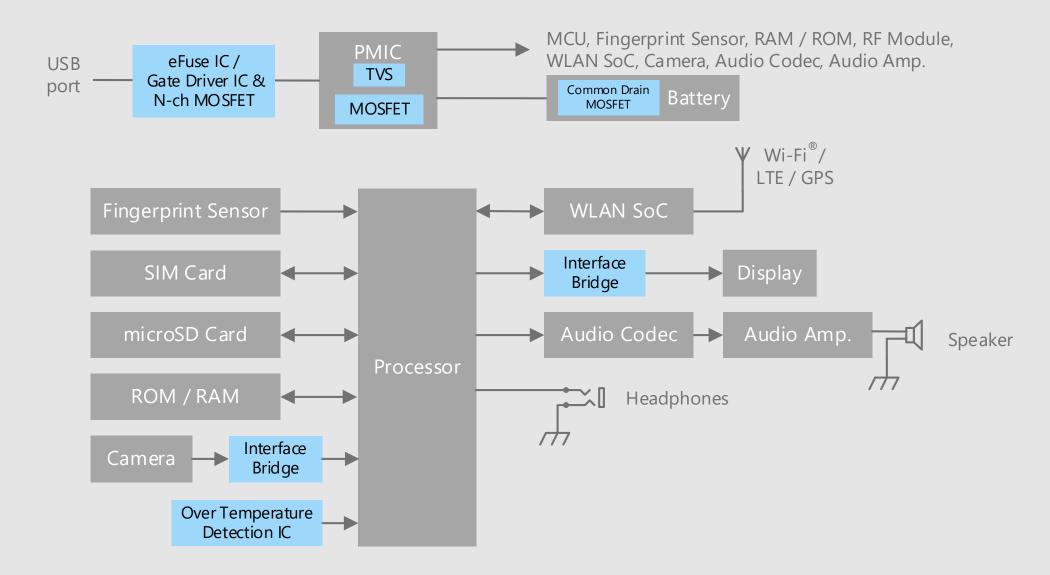
Toshiba Electronic Devices & Storage Corporation provides comprehensive device solutions to customers developing new products by applying its thorough understanding of the systems acquired through the analysis of basic product designs.



Block Diagram

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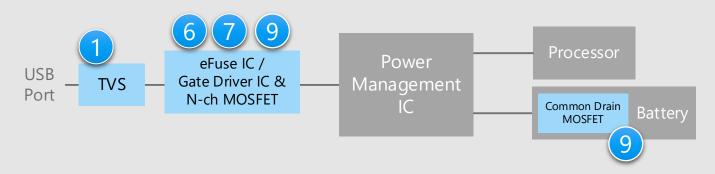
Tablet Device Overall block diagram



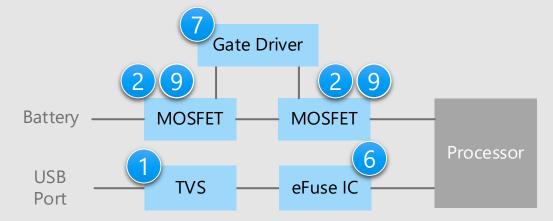
Tablet Device Details of power supply unit

System power circuit

Method using power controller



Method without power controller



* Click on the blue circled numbers above to view detailed descriptions.

Criteria for device selection

- TVS diodes are suitable for ESD protection of power line.
- MOSFETs with low on-resistance are suitable for the control of USB and battery powered supply circuits.
- Small package products contribute to the reduction of circuit board area.

- Prevent circuit malfunctions by absorbing static electricity from external terminals
 TVS diode
- Realize the set with low power consumption by low on-resistance
 Small signal MOSFET
- Built-in protection function against short circuit, over current, over voltage, etc.
 Electronic fuse (eFuse IC)
- Small package and built-in over voltage protection function
 N-ch MOSFET gate driver IC
- Low on-resistance and small package
 N-ch common drain MOSFET



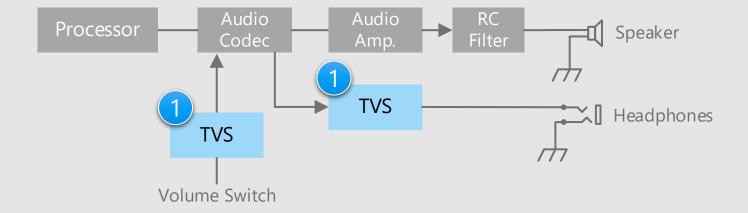






Tablet Device Detail of audio unit

Audio unit circuit



Criteria for device selection

- TVS diodes are suitable for ESD protection of signal line.
- Small package products contribute to the reduction of circuit board area.

Proposal from Toshiba

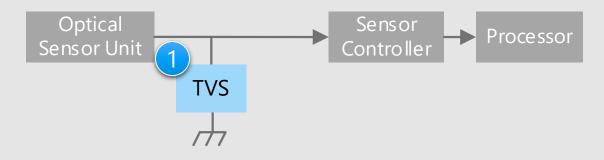
Prevent circuit malfunctions by absorbing static electricity from external terminals
 TVS diode

^{*} Click on the blue circled numbers above to view detailed descriptions.

Tablet Device Details of touch sensor unit

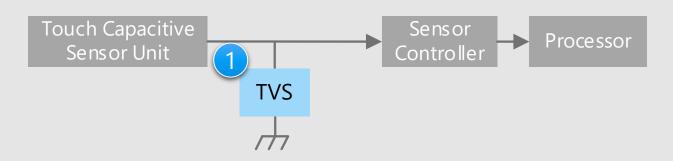
Touch sensor circuit

Optical type



Touch sensor circuit

Capacitive type



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Criteria for device selection

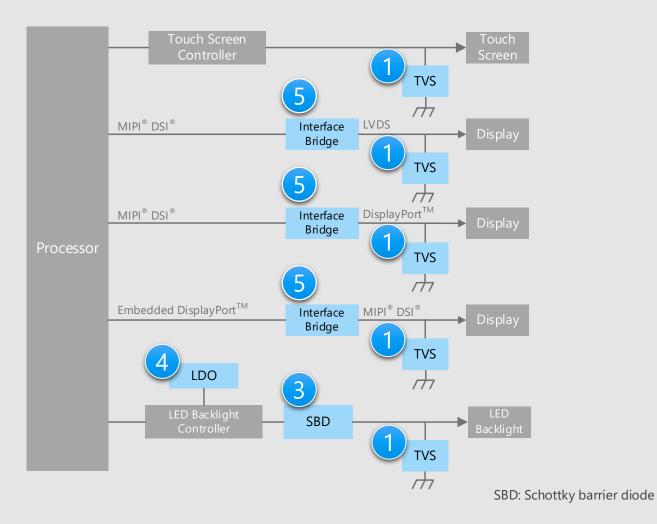
- TVS diodes are suitable for ESD protection of signal line.
- Small package products contribute to the reduction of circuit board area.

Proposal from Toshiba

Prevent circuit malfunctions by absorbing static electricity from external terminals
 TVS diode

Tablet Device Detail of display unit

Display unit circuit



^{*} Click on the blue circled numbers above to view detailed descriptions.

Criteria for device selection

- TVS diodes are suitable for ESD protection of signal line.
- By using a Schottky barrier diode with low V_F and low I_R, the power consumption of the set can be reduced.
- Small package products contribute to the reduction of circuit board area.
- By using interface bridge, display and camera components can be selected without any concern for interface standards.

- Prevent circuit malfunctions by absorbing static electricity from external terminals

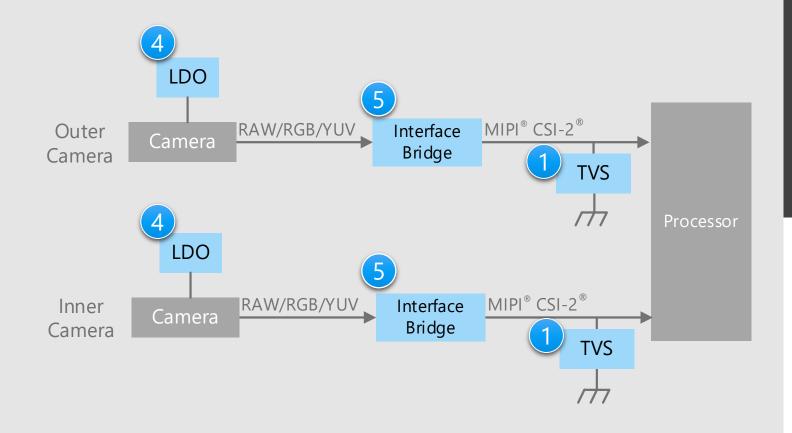
 TVS diode
- High speed, low loss
 Schottky barrier diode
- Supply the power with low noise Small surface mount LDO regulator
- Eliminate differences between interfaces
 Interface bridge

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Tablet Device Detail of camera unit

Camera unit circuit



Criteria for device selection

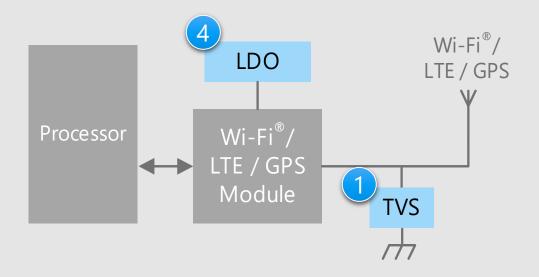
- PSRR (Power Supply Rejection Ratio) of LDO regulator is an important parameter for camera modules.
- TVS diodes are suitable for ESD protection of signal line.
- Small package products contribute to the reduction of circuit board area.
- By using interface bridge, display and camera components can be selected without any concern for interface standards.

- Prevent circuit malfunctions by absorbing static electricity from external terminals
 TVS diode
- Supply the power with low noise Small surface mount LDO regulator
- Eliminate differences between interfaces Interface bridge

^{*} Click on the blue circled numbers above to view detailed descriptions.

Tablet Device Detail of wireless unit

Wireless communication circuit



Criteria for device selection

- Small package products contribute to the reduction of circuit board area.
- A small TVS diode with low C_t is suitable for ESD protection without attenuating the antenna signal.
- LDO regulator that can supply large current is suitable for the wireless module.

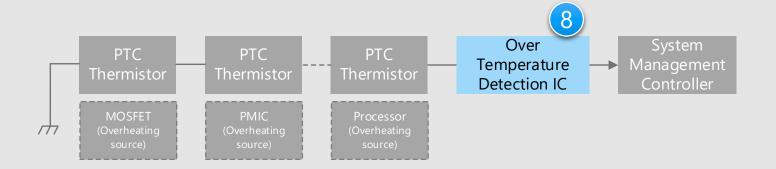
- Prevent circuit malfunctions by absorbing static electricity from external terminals
 TVS diode
- Supply the power with low noise
 Small surface mount LDO regulator

Il surface mount LDO regulator

^{*} Click on the blue circled numbers above to view detailed descriptions.

Tablet Device Detail of over temperature monitoring unit

Over temperature monitoring circuit



Criteria for device selection

- Over temperature monitoring is required at multiple points on the circuit board such as MOSFET, PMIC and Processor.
- Low power dissipation of set can be realized by using the over temperature detection IC with low current consumption.
- Small package products contribute to the reduction of circuit board area.

Proposal from Toshiba

 Monitor increase of temperature at multiple points with small package and low current consumption

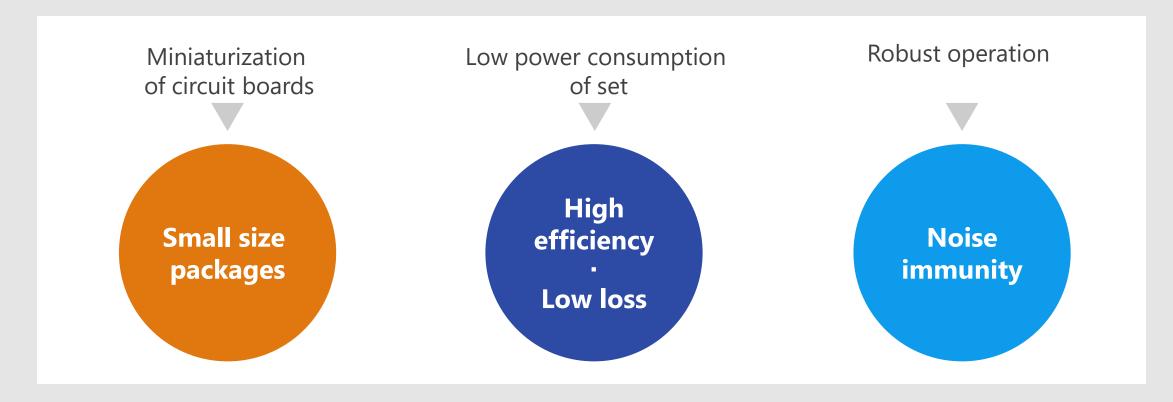
Over temperature detection IC (ThermoflaggerTM)

^{*} Click on the blue circled numbers above to view detailed descriptions.



Device solutions to address customer needs

As described above, in the design of tablet device, "Miniaturization of circuit boards", "Low power consumption of set" and "Robust operation" are important factors. Toshiba's proposals are based on these three solution perspectives.



Device solutions to address customer needs

	Small size packages	High efficiency . Low loss	Noise immunity
① TVS diode			
2 Small signal MOSFET			
3 Schottky barrier diode			
4 Small surface mount LDO regulator			
5 Interface bridge			
6 Electronic fuse (eFuse IC)			
7 N-ch MOSFET gate driver IC			
Over temperature detection IC (Thermoflagger TM)			
9 N-ch common drain MOSFET			







Absorbs static electricity from external terminals, prevents circuit malfunction and protects devices.

Improved ESD pulse absorption

Improved ESD absorption compared to Toshiba's existing products. (50 % reduction in operating resistance) For some products, both low operating resistance and low capacitance are realized and ensures high signal protection performance and signal quality.

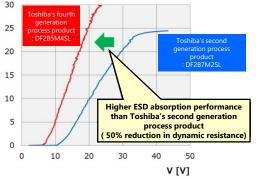
Suppress ESD energy by low clamp voltage

Protect the connected circuits and devices using Toshiba own technology.

Suitable for high density mounting

A variety of small packages are available.

ESD Pulse Absorption Performance (Toshiba internal comparison)



Unidirectional



Suitable for paths such as logic signals. There are lineups of 1in1, 2in1, 4in1, 5in1, 7in1.

Bidirectional



Suitable for paths with both polar signals such as audio signals.

Lineup						
Part number	DF2B7ASL	DF2B5M4ASL	DF2B6M4ASL	DF2B6M4BSL		
Package	SL2	SL2	SL2	SL2		
V _{ESD} [kV]	±30	±16	±15	±8		
V _{RWM} (Max) [V]	5.5	3.6	5.5	5.5		
C _t (Typ.) [pF]	8.5	0.15	0.15	0.12		
R _{DYN} (Typ.) [Ω]	0.2	0.7	0.7	1.05		

◆Return to Block Diagram TOP

(Note) This product is an ESD protection diode and cannot be used for purposes other than ESD protection.







Suitable for power management and contributes to miniaturization.

Low voltage operation

Operates down to $|V_{GS}| = 4.5 \text{ V}$

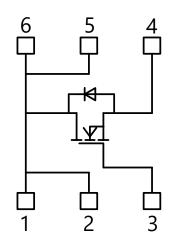
DescriptionLow on-resistance

On-resistance between the drain and source is low. Heat generation and power consumption can be kept low.

3 Small package

Sealed in SOT-1220 / SOT-1118 (2.0 x 2.0 mm) packages.

Internal circuit SSM6K513NU



Lineup									
Part number		SSM6K513NU	l	SSM6N5	5NU	SSM6J507NU			
Package		UDFN6B (SOT-1220)		UDFN6 (SOT-1118)		UDFN6B (SOT-1220)	*		
Polarity		N-ch		N-ch x 2		P-cl	า		
V _{DSS} [V]		30		30		-30			
I _D [A]		15		4		4		-10	
$R_{DS(ON)}$ [m Ω]	Тур.	8.0		48		19			
$@ V_{GS} = 4.5 \text{ V}$	Max	12		64		28			

Schottky barrier diode CUHS20F40 / CTS05F40







Value provided

Can be applied to various applications which require high speed and low loss. And contribute to miniaturization.

Fast switching

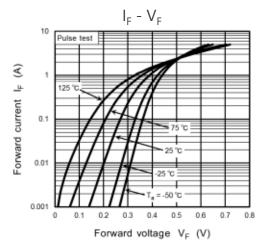
For fast switching applications.

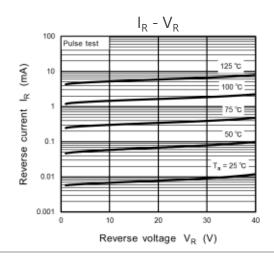
Small package

Small surface mount packages for high density assembly:

US2H: 2.5 x 1.4 x 0.6 mm CST2: 1.0 x 0.6 x 0.38 mm

CUHS20F40 Characteristics Curves





Lineup								
Part number	CUHS20F40	CTS05F40						
Package	US2H	CST2						
I _O [A]	2.0	0.5						
V _R [V]	40	40						
V _F (Typ.) [V]	0.39 @I _F = 1.0 A	0.74 @I _F = 0.5 A						
I_R (Max) [μ A] @ V_R = 40 V	60	15						







Wide lineup from general purpose type to small package type are provided. Contribute to realize a stable power supply not affected by fluctuation of battery.

Low dropout voltage

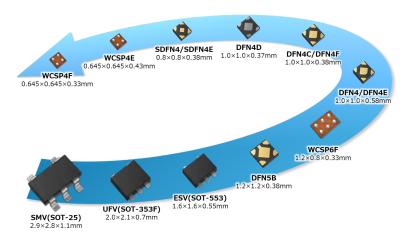
The originally developed process significantly improved the dropout voltage characteristics.

High PSRR Low output noise voltage

Many product series that realize both high PSRR (Power Supply Rejection Ratio) and low output noise voltage characteristics are provided. They are suitable for stable power supply for analog circuit. 3 Low current consumption

 $0.34~\mu A$ of $I_{B(ON)}$ is realized by utilizing CMOS process and unique circuit technology. (TCR3U Series)

Rich package lineup



Lineup										
Part number	TCR15AG Series	TCR8BM Series	TCR5FM Series	TCR5RG Series	TCR3RM Series	TCR3U Series	TCR3LM Series	TCR3D Series	TCR3EM Series	TCR1HF Series
Features	volt	ropout age PSRR	High PSRR Low noise Low current consumption		Low current Standard type			36 V Input voltage		
I _{OUT} (Max) [A]	1.5	0.8	0.	0.5			0.3			0.15
PSRR (Typ.) [dB] @f = 1 kHz	95	98	91	100	100	70	-	72	68	70
I _B (Typ.) [μΑ]	25	20	10	7	7	0.34	1	86	35	170







Eliminating the interface gap between host and display/camera allows more options of component selection.

Wider component selection

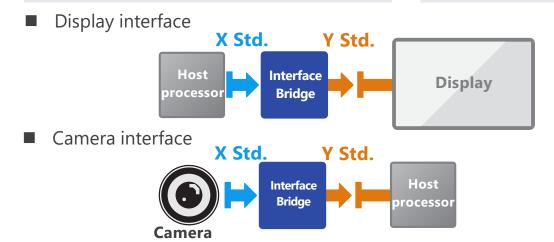
Conversion of the interface allows shared procurement with other products as well as adoption of less inexpensive parts.

Noise immunity

Converting parallel communication to serial improves noise immunity and suppresses noise generation to the surroundings.

3 Less cabling

Converting from parallel communication to serial reduces total number of wires and the risks of wire breakage.



Lineup								
Part number	TC358775XBG	TC358767AXBG	TC358860XBG	TC358746AXBG				
Package	BGA64	BGA81	BGA65	BGA72				
Input	MIPI [®] DSI [®] 1.01	(1) MIPI [®] DSI [®] 1.01 (2) MIPI [®] DPI SM 2.0	VESA Embedded DisplayPort™ (eDP)	(1) MIPI® CSI-2® (2) Parallel 24bit				
Output	LVDS Dual Link (5 pairs / link)	VESA DisplayPort™ 1.1a	MIPI [®] DSI [®] 1.02	(1) Parallel 24bit (2) MIPI® CSI-2®				







Electronic fuse (eFuse IC) can be used repeatedly to protect circuits from abnormal conditions such as overcurrent and overvoltage.

Can be used repeatedly

When overcurrent flows through the electronic fuse (eFuse IC), the internal detection circuit operates and switches off the internal MOSFET. It is not destroyed by a single overcurrent and can be used repeatedly.

TEC 62368-1 certified

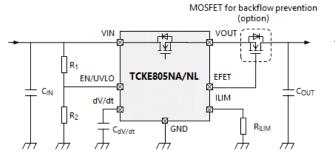
Toshiba's eFuse ICs are certified to the international safety standard IEC 62368-1 (G9: Integrated circuit (IC) current limiters) and contribute to robust protection and simplification of circuit design.

3 Various protection functions

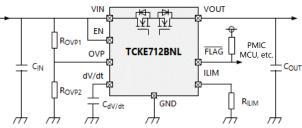
TCKE8 Series: Short-circuit protection, overcurrent protection, overcurrent clamp function, overvoltage clamp function, thermal shut down, inrush current suppression, backflow prevention (optional), etc.

TCKE7 Series: Short-circuit protection, overcurrent protection, overvoltage protection, thermal shut down, FLAG signal output, backflow prevention (built-in), etc.

Reference circuit example of TCKE8 Series



Reference circuit example of TCKE7 Series



Lineup					
Part number	TCKE800NA/NL	TCKE805NA/NL	TCKE812NA/NL	TCKE712BNL	
Package	WSON10B 3.0 x 3.0 x 0.75 mi	m	ren .	WSON10 3.0 x 3.0 x 0.75 mm	min
V _{IN} [V]		4.4 to 18			
R_{ON} (Typ.) [m Ω]		28			
Return function	NL: Latch	Latch type (external signal control)			
V _{OVC} (Typ.) [V]	-	6.04	15.1	Adjustable	



It is N-ch MOSFET gate driver IC with OVP [Note 1] function. It contributes to reduction of power consumption and miniaturization of load switch circuit.

[Note 1] OVP: Over Voltage Protection

Three types of N-ch MOSFET can be driven

The following types of MOSFET can be driven: TCK40xG: Single high side connection
Common source connection
TCK42xG: Single high side connection
Common drain connection

Wide operating voltage range and various OVLO [Note 2] threshold voltage

Operating voltage V_{opr} : 2.7 to 28 V Maximum input voltage: 40 V V_{IN_OVLO} [Note 3] lineups suitable for 5 to 24V power supply line.

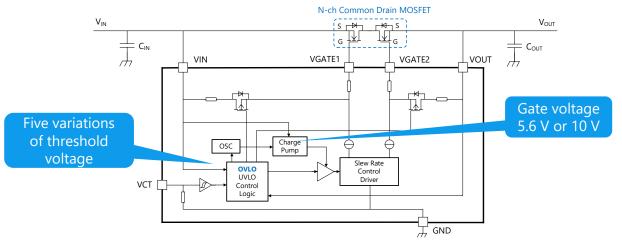
[Note 2] OVLO: Over Voltage Lock Out [Note 3] V_{IN OVLO}: V_{IN} OVLO threshold

3 Small packages

It contributes to reduction of the mounting area and miniaturization of the circuit board:

WCSP6E: 1.2 x 0.8 mm, t: 0.55 mm WCSP6G: 1.2 x 0.8 mm, t: 0.35 mm

Circuit example of TCK42xG with N-ch common drain connection MOSFET



Lineup					
Part number	V _{IN_OVLO} Min / Max [V]	V _{GS} Typ. / Max [V]	N-ch MOSFET type can be driven	Package	9
TCK401G	Over 28	Max 10	Single high side	WCSP6E	
TCK402G	Over 20	$(V_{IN} \ge 12 V)$	Common Source	VVC3F0E	0
TCK420G	26.50 / 28.50	10 / 11			
TCK421G	22.34 / 24.05	10 / 11 (V _{IN} ≥ 5 V)			
TCK422G	13.61 / 14.91	(VIN 2 3 V)	Single high side	WCCDCC	4
TCK423G	13.61 / 14.91		Common Drain	WCSP6G	
TCK424G	10.35 / 11.47	5.6 / 6.3			
TCK425G	5.76 / 6.87				

8 Over temperature detection IC (ThermoflaggerTM)







Value provided

TCTH series can detect temperature rise at multiple points on the circuit board.

Temperature rise can be detected at multiple points

TCTH series detect voltage drop of PTC (Positive Temperature Coefficient) thermistors due to temperature rise by supplying a constant current (1 μ A or 10 μ A) to PTC thermistors. Multiple PTC thermistors connected in series enable to detect temperature rise at multiple points on the circuit board.

Example of temperature rise detecting circuit by ThermoflaggerTM. thermistor PTC Detection thermistor Detection temperature temperature 120°C Detection 120°C temperature 80°C PTC **Over temperature** thermistor detection IC Thermoflagger[™] 100°C

2 Low current consumption and small package

TCTH01 series has I_{DD} = 1.8 μ A (Typ.) and TCTH02 series has I_{DD} = 11.3 μ A (Typ.). These packages are small size ESV type.

Lineup						
Part number	TCTH011AE/BE	TCTH012AE/BE	TCTH021AE/BE	TCTH022AE/BE		
Package	ESV 1.6 x 1.6 x 0.55 mm					
V _{DD} [V]	1.7 to 5.5					
I _{DD} (Typ.) [μΑ]	1.8 11.3					
PTCO Output current (Typ.) [μA]	1	1	10	10		
Abnormal latch function	-	Yes	-	Yes		
Output circuit type	AE: push pull, BE: open drain					







This is low on-resistance MOSFET with small and thin package. It contributes to suppressing heat generation during charging and discharging, as well as to reducing the size of set.

Low on-resistance

Low on-resistance is achieved by applying a low resistance diffusion process.

This contributes to suppression of heat generation.

Small and thin package

While in a dual configuration, it is a small and thin chipLGA package products.

This contributes to miniaturization of set.

2 Low gate-source leakage current

Low gate-source leakage current characteristics enable low standby power and contribute to long term operation of battery used sets.

Examples of common drain MOSFET application

Power multiplexer Li-ion battery protection circuit SSM10N961L Gate Driver TCK42xG VINB Frotection Drain MOSFET Battery B Gate Driver B Gate Driver Drain MOSFET B HOW TO STEEL TO

Lineup							
Part number	SSM14N956L	SSM10N954L	SSM6N951L	SSM10N961L			
Package	inn			.::::			
	TCSPED-302701	TCSPAC-153001	TCSP6A-172101	TCSPAG-341501			
Source-source voltage V _{SSS} [V]		12		30			
Gate-source voltage V _{GSS} [V]		±8		±20			
Source current (DC) I _S [A]	20.0	13.5	8.0	14.0			
$R_{SS(ON)}$ (Typ.) [m Ω] @ V_{GS} = 3.8 V	1.1	2.2	4.6	-			
$R_{SS(ON)}$ (Typ.) [m Ω] @ V_{GS} = 10 V	-	-	-	9.9			

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