TOSHIBA MULTI CHIP DISCRETE DEVICE

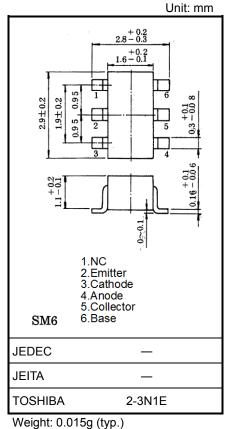
HN2E04F

Super High Speed Switching Application Audio Frequency Amplifier Application Audio Low Noise Amplifier Application **O1**

Q.			
	High Voltage	: VCEO) = -120V
	High DC Current Gain	: hFE =	200 to 700
	Good hFE Linearity	: hFE(lo	C =- 0.1mA)/ hFE(IC =- 2mA) = 0.95
Q2	2		
	Low Forward Voltage Drop		: VF(3) = 0.98V (typ.)
	Fast Reverse Recovery Time		: trr = 1.6ns (typ.)
	Low Total Capacitance		: CT = 0.5pF (typ.)
Q1	(Transistor)	: 2SA1	587 equivalent
Q2	(Diode)	: 1SS3	52 equivalent

Q1 (Transistor) Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	Vсво	-120	V
Collector-emitter voltage	VCEO	-120	V
Emitter-base voltage	VEBO	-5	V
Collector current	IC	-100	mA
Base current	Ι _Β	-20	mA



Q2 (Diode) Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Maximum (peak) reverse voltage	V _{RM}	85	V
Reverse voltage	VR	80	V
Maximum (peak) forward current	I _{FM}	300	mA
Average forward current	IO	100	mA
Surge current (10ms)	IFSM	1	А

Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 Common)

Characteristic	Symbol	Rating	Unit	
Collector power dissipation	Pc*	300	mW	
Junction temperature	Tj	125	°C	
Storage temperature range	T _{stg}	-55 to 125	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

*Total rating: Power dissipation per element should not exceed 200mW per element.

Start of commercial production 2000-02

Q1 (Transistor) Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	ICBO	_	$V_{CB} = -120V, I_E = 0A$	_	_	-100	nA
Emitter cut-off current	IEBO	_	V_{EB} = -5V, I _C = 0A	_	_	-100	nA
DC current gain	h _{FE} *	_	V _{CE} =- 6V, I _C = -2mA	200	_	700	
Collector-emitter saturation voltage	VCE(sat)	_	IC =-10mA, IB =-1mA	_	_	-0.3	V
Transition Frequency	fŢ	—	$V_{CE} = -6V$, IC = $-1mA$	_	100	—	MHz
Collector Output Capacitance	Cob	—	V _{CB} =–10V, I _E = 0A, f=1MHz	_	4	—	pF
Noise figure	NF	_	$\label{eq:VCE} \begin{split} V_{CE} &= -6 \text{ V, } \text{I}_{C} = -0.1 \text{ mA} \\ \text{f} &= 1 \text{ kHz, } \text{R}_{g} = 10 \text{ k} \Omega \end{split}$	_	1.0	_	dB

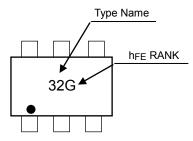
*: hFE Classifications GR(G): 200 to 400 , BL(L): 350 to 700 ()Ma

()Marking Symbol

Q2 (Diode) Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
	V _{F (1)}	_	I _F = 1mA		0.62		
Forward voltage	V _{F (2)}	-	I _F = 10mA		0.75		V
	V _{F (3)}	-	I _F = 100mA	-	0.98	1.20	
Reverse current	IR (1)	_	V _R = 30V	-	—	0.1	μA
Reverse current	IR (2)	-	V _R = 80V	-	_	0.5	
Total capacitance	Ст	_	V _R = 0V, f = 1MHz	_	0.5	_	pF
Reverse recovery time	t _{rr}	_	I _F = 10mA (fig.1)	_	1.6	_	ns

Marking



Equivalent Circuit (Top View)

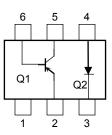
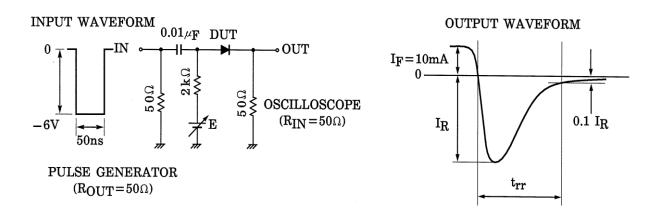


Fig. 1: Reverse Recovery Time (trr) Test Circuit



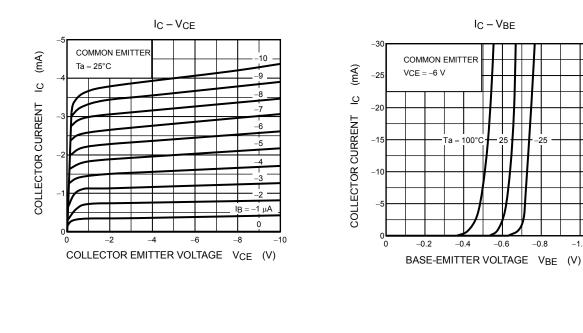
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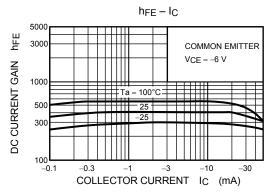
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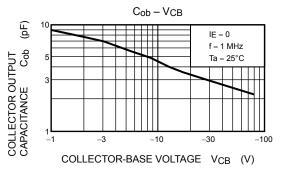
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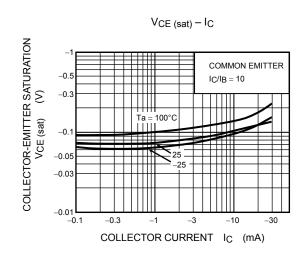
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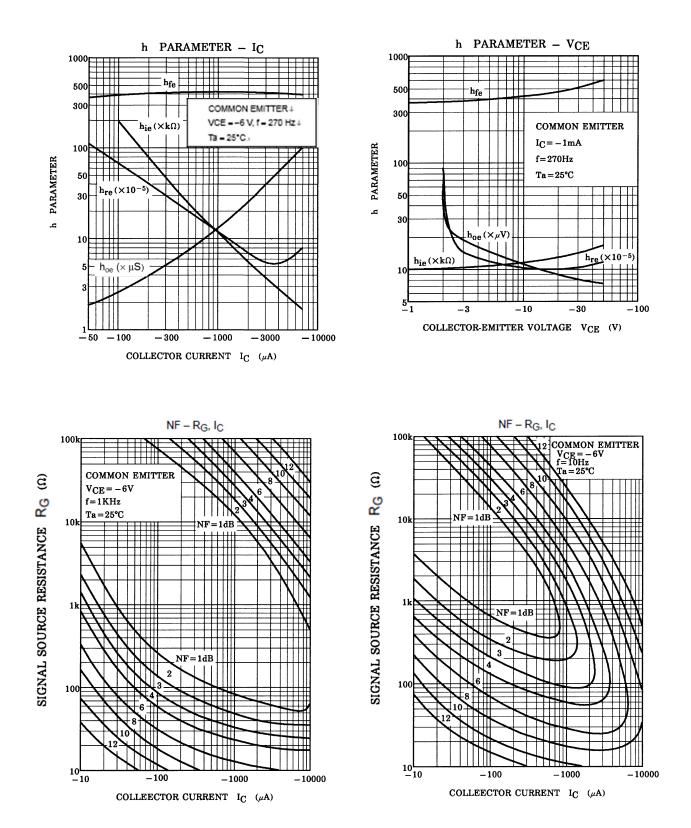
Q1



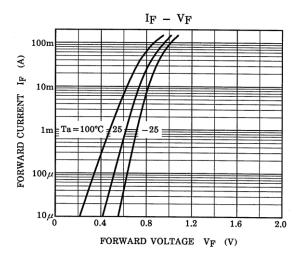


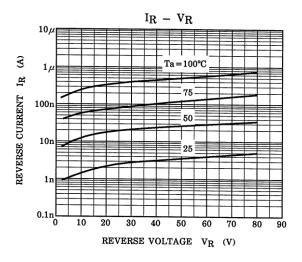


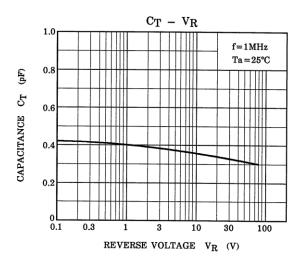




Q2

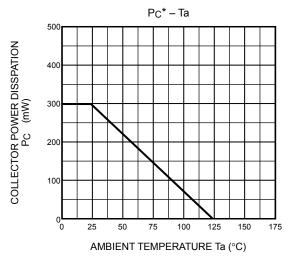






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Q1, Q2 Common



*Total Rating.

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